

2541
28/2/17



GOA UNIVERSITY
Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - III EXAMINATION
HELD IN NOVEMBER 2016

Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No:	P R No:	Sex:	Name:	No Of Credits	Grade Obtained	SGPA
3301	201507401	M	BHASALE VINAY BHANUDAS			
No Of Attempts : 1						
System on Chip (SOC)						
		Theory	4	AB	P	
		IA	2	AB	P	
TCP/IP and Implementation of a Network IP on FPGA						
		Theory	4	BB	P	
		IA	2	AB	P	
Project						
		IA	4	AA	P	
		Oral	8	AA	P	
Total :						24
						8.33 P
						PASSES
3302	201107681	M	BORKAR NEEL RAMAKANT			
No Of Attempts : 1						
System on Chip (SOC)						
		Theory	4	AB	P	
		IA	2	BC	P	
TCP/IP and Implementation of a Network IP on FPGA						
		Theory	4	AA	P	
		IA	2	AA	P	
Project						
		IA	4	AB	P	
		Oral	8	AB	P	
Total :						24
						8.08 P
						PASSES
3303	201108381	F	GOVEKAR DIVYA DIGAMBAR			
No Of Attempts : 1						
System on Chip (SOC)						
		Theory	4	BC	P	
		IA	2	CC	P	
TCP/IP and Implementation of a Network IP on FPGA						
		Theory	4	CC	P	
		IA	2	BC	P	
Project						
		IA	4	AA	P	
		Oral	8	AB	P	
Total :						24
						6.92 P
						PASSES
3304	201006305	F	KALSHAONKAR REEMA DATTARAM			
No Of Attempts : 1						
System on Chip (SOC)						
		Theory	4	AA	P	
		IA	2	AA	P	
TCP/IP and Implementation of a Network IP on FPGA						
		Theory	4	AB	P	
		IA	2	AO	P	
Project						
		IA	4	AA	P	
		Oral	8	AA	P	
Total :						24
						8.92 P
						PASSES

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28/2/17

P: Passes; F: Fails; A/ABS: Absent; NNAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cumulative Grade Point Average

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Seat No :	P R No :	Sex :	Name :	No Of Credits	Grade Obtained	SGPA
3305	200907827	F	KHANDEPARKAR SHWETA MAHENDRA			
No Of Attempts : 1						
System on Chip (SOC)		Theory	4	AB	P	
		IA	2	BB	P	
TCP/IP and Implementation of a Network		IP on FPGA				
		Theory	4	BB	P	
		IA	2	AA	P	
Project		IA	4	AB	P	
		Oral	8	BB	P	
Total :			24			7.50 P PASSES

Seat No :	P R No :	Sex :	Name :	No Of Credits	Grade Obtained	SGPA
3306	201008499	F	KHARANGATE LAXIMI SHASHANK			
No Of Attempts : 1						
System on Chip (SOC)		Theory	4	AA	P	
		IA	2	AA	P	
TCP/IP and Implementation of a Network		IP on FPGA				
		Theory	4	AA	P	
		IA	2	AO	P	
Project		IA	4	AA	P	
		Oral	8	AA	P	
Total :			24			9.08 P PASSES

Seat No :	P R No :	Sex :	Name :	No Of Credits	Grade Obtained	SGPA
3307	201105539	M	KORDE CHARUDATTA GURUDAS			
No Of Attempts : 1						
System on Chip (SOC)		Theory	4	AA	P	
		IA	2	AA	P	
TCP/IP and Implementation of a Network		IP on FPGA				
		Theory	4	BC	P	
		IA	2	AO	P	
Project		IA	4	AA	P	
		Oral	8	AO	P	
Total :			24			8.92 P PASSES

Seat No :	P R No :	Sex :	Name :	No Of Credits	Grade Obtained	SGPA
3308	201107707	M	MAHATME MOHIT BHAIKAV			
No Of Attempts : 1						
System on Chip (SOC)		Theory	4	AB	P	
		IA	2	AA	P	
TCP/IP and Implementation of a Network		IP on FPGA				
		Theory	4	BB	P	
		IA	2	AA	P	
Project		IA	4	AB	P	
		Oral	8	AB	P	
Total :			24			8.00 P PASSES

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Seat No :	P R No :	Sex :	Name :		
No Of Attempts :				No Of Credits	Grade Obtained
				SGPA	
3309	201007353	F	MAYEKAR MITHILI NARAYAN		
				6.50 P	
				PASSES	
System on Chip (SOC)		Theory	4	BB	P
		IA	2	CC	P
TCP/IP and Implementation of a Network		IP on FPGA	4	CC	P
		IA	2	BB	P
Project		IA	4	BB	P
		Oral	8	BB	P
Total :				24	
3310	201006308	F	NAIK STHRIGDHARA NAVANATH		
				7.58 P	
				PASSES	
System on Chip (SOC)		Theory	4	AB	P
		IA	2	AA	P
TCP/IP and Implementation of a Network		IP on FPGA	4	AB	P
		IA	2	AB	P
Project		IA	4	BB	P
		Oral	8	BB	P
Total :				24	
3311	201008518	F	NAIK TEJA UDAY		
				7.92 P	
				PASSES	
System on Chip (SOC)		Theory	4	AB	P
		IA	2	AA	P
TCP/IP and Implementation of a Network		IP on FPGA	4	AA	P
		IA	2	AB	P
Project		IA	4	AB	P
		Oral	8	BB	P
Total :				24	
3312	201107719	M	PATIL ABHIJEET VILAS		
				6.67 P	
				PASSES	
System on Chip (SOC)		Theory	4	BC	P
		IA	2	BC	P
TCP/IP and Implementation of a Network		IP on FPGA	4	BB	P
		IA	2	BC	P
Project		IA	4	BB	P
		Oral	8	BB	P
Total :				24	

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KH



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Seat No: 3313 P R No: 201105565 Sex: M Name: RANE KARAN PARSHURAM
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
Image Processing	Theory	4	AA P	
	IA	2	AA P	
TCP/IP and Implementation of a Network IP on FPGA	Theory	4	AB P	
	IA	2	AA P	
Project	IA	4	AA P	
	Oral	8	AA P	
Total :		24		8.83 P PASSES

Seat No: 3314 P R No: 201106312 Sex: M Name: SAWAL SAGAR NAMDEV
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
System on Chip (SOC)	Theory	4	BB P	
	IA	2	AA P	
TCP/IP and Implementation of a Network IP on FPGA	Theory	4	CC P	
	IA	2	AB P	
Project	IA	4	AA P	
	Oral	8	AB P	
Total :		24		7.58 P PASSES

Seat No: 3315 P R No: 201007394 Sex: M Name: SHETGAONKAR ANAY ANIL
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
System on Chip (SOC)	Theory	4	BC P	
	IA	2	CC P	
TCP/IP and Implementation of a Network IP on FPGA	Theory	4	FF F	
	IA	2	CC P	
Project	IA	4	AA P	
	Oral	8	AA P	
Total :		24		6.33 F FAILS

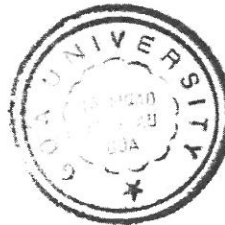
Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By: *P. Pinto*
Checked By: *Kul*
Date: 23/2/17

S.S.J. Figueiredo
22/2/2017
S.S.J. Figueiredo
Assistant Registrar-E(Proff.)

Leo V. Macedo
23/2/17
Leo V. Macedo
Controller Of Examinations

Y.V. Reddy
23/2/17
Prof. Y. V. Reddy
Registrar



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